



SMALL SIGNAL N-CHANNEL J-FET IN A HERMETICALLY SEALED **CERAMIC SURFACE MOUNT PACKAGE** FOR HIGH RELIABILITY APPLICATIONS

MECHANICAL DATA Dimensions in mm (inches)

$\frac{0.51 \pm 0.10}{(0.02 \pm 0.004)}$ 0.31 (0.012) rad. $\frac{1.91 \pm 0.10}{(0.075 \pm 0.004)}$ 0.31 (0.012) rad. 3.05 ± 0.13 (0.12 ± 0.005) 1.40 (0.055) 1.02 ± 0.10 (0.04 ± 0.004)

FEATURES

- HERMETIC CERAMIC SURFACE MOUNT PACKAGE (SOT23 COMPATIBLE)
- CECC SCREENING OPTIONS
- SPACE QUALITY LEVELS OPTIONS

SOT23 CERAMIC (LCC1 PACKAGE)

Underside View

PAD 1 – Source

PAD 2 – Drain

PAD 3 - Gate

APPLICATIONS:

Hermetically sealed surface mount version of the popular 2N4392 for high reliability / space applications requiring small size and low weight devices.

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25°C unless otherwise stated)

V_{DS}	Drain – Source Voltage	40V
V_{DG}	Drain – Gate Voltage	40V
V_{GS}	Gate – Source Voltage	40V
I_{G}	Forward Gate Current	50mA
P_{D}	Power Dissipation @ T _A = 25°C	500mW
	Derate above 25°C	2.85mW / °C
T_J , T_STG	Operating Junction and Storage Temperature Range	−65 to +175°C

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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ELECTRICAL CHARACTERISTICS (T_{amb} = 25°C unless otherwise stated)

	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
V _{(BR)GSS}	Gate – Source Breakdown Voltage	$V_{DS} = 0$	I _G = 1μA	40			
V_{GS}	Gate – Source Voltage	V _{DS} = 20V	I _D = 1nA	-2		– 5	\ \ \
V _{GS(f)}	Gate – Source Forward Voltage	$V_{DS} = 0$	I _G = 1mA			1]
I _{GSS}	Gate Reverse Current	$V_{DS} = 0$	V _{GS} = 20V			0.1	nA
I _{D(off)}	Drain Cut-off Current	V _{DS} = 20V	$V_{GS} = -7V$			0.1	
I _{DSS*}	Zero Gate Voltage Drain Current	V _{DS} = 20V	V _{GS} = 0	25		75	mA
V _{DS(on)}	Drain – Source On Voltage	$V_{GS} = 0$	I _D = 6mA			0.4	V
R _{DS(on)}	Drain – Source On Resistance	$V_{GS} = 0$	I _D = 1mA			60	Ω
C _{ISS}	Input Capacitance	V _{DS} = 20V	V _{GS} = 0			14	- pF
		f = 1MHz					
C _{RSS}	Reverse Transfer Capacitance	$V_{DS} = 0$	$V_{GS} = -7V$			3.5	
		f = 1MHz					
R _{DS(on)}	Static Drain – Source On Resistance	$V_{GS} = 0$	$I_D = 1mA$			60	Ω
t _r	Rise Time	$I_{D(on)} = 6mA$				5	
t _f	Fall Time	$V_{GS(off)} = 7V$				20	ns
t _{on}	Turn-On Time	$I_{D(on)} = 6mA$				15	''5
t _{off}	Turn-Off Time	$V_{GS(off)} = 7V$				35	

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